



PATENT APPLICATION
Docket No. 9898-204
Client N . SS-16038-US

In re application of Jae-Y on Sim and Jei-Hwan Yoo
Serial No. 09/901,930 Examiner: Terry D. Cunningham
Confirmation No. 7100
Filed: July 9, 2001 Art Unit: 2816
For: NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR
MEMORY DEVICE

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)
TDC	—	Article from Journal of Solid-State Circuits, Vol. 24, No. 3, June 1989, page 597-602. Entitled: A1-Mbit BiCMOS DRAM Using Temperature-Compensation Circuit Techniques.

Examiner: T. Cunningham

Date Considered: 6/14/04

RECEIVED
DEC 29 2003
TC 2800 MAIL ROOM